

ASIC (RTL) DESIGN LEAD

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Company: elInfochips (An Arrow Company)

Location: India

Category: other-general

ASIC RTL Design Lead (ONLY IMMEDIATE JOINERS) Job Location : Bangalore, Hyderabad, Pune, Noida, Ahmedabad, Chennai 12-15 years of experience in ASIC design and SoC integration Good knowledge of PCIe, HBM, and Processor subsystem integration Lint, CDC Constraint updation (IP to SoC level), Synthesis and timing analysis PD Support

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