

## ASIC Verification Lead - System Verilog

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Company: Nexus Professionals

Location: Hyderabad/Bangalore/Chennai/Ahmedabad

Category: computer-and-mathematical

About the client:- Company Name:Client of Nexus Professionals - Industries:IT Services & Consulting, Recruitment / Staffing - Company Type: Startup - Funding Stage: Other - Headquarter Location: Hyderabad - Nature of Offering: Service - Founding Year: 2021 - No of Employees:11-50 Job Description :Asic/Soc Verification8 to 15 years of experience in SOC/IP/block level functional verification using System Verilog/UVM.Strong knowledge of UVM, advance UVM, System Verilog.Must have worked on development of testplan, testbench components, verification environment, interface agents, Scoreboard in UVM.Understanding of complete functional verification cycle will be added advantageKnowledge of at least one industry standard protocols like Ethernet, PCIe, MIPI, USB, AMBA or similar is required.Skills to debug RTL & testbench issues, test failures.Experience on verification closure by closing Coverage and bug reportsPrimarily knowledge of Script development and maintenanceUnderstanding of customer dynamic environment change and adopt run time changes in schedule, design etcMUST Be willing to Lead a Team of min 4-5 Engineers. (ref:hirist.tech)

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