

Emulation Lead

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Company: Wipro

Location: bangalore

Category: other-general

Job Description A good understanding of architectural aspects and RTL code at IP/Sub-system/SoC level A good understanding of verification methodologies including SV-UVM/C based environment, transactors etc Experience in building emulation models from scratch Knowledge of Arm CPU cores, protocols including PCIe, USB, Ethernet, AMBA, UART/SPI/I2C, DDR, flash memories and their usage in SoC environments is necessary Emulation experience on ZeBu/Veloce/Palladium, including compilation, test execution, debug, and performance. Strong scripting skills including UNIX shell scripting, Perl, TCL etc Support various emulation users Must have worked in end-to-end Emulation of atleast 1-2 SoC programs in Sub-system and chip level. Locations: India, Hyd, Delhi (NCR), Chennai, Bangalore, Pune, Kochi, Mumbai, Etc). Experience: 5-14 Years

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