

RF Digital Design Engineer

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Company: QpiAI

Location: Bengaluru

Category: architecture-and-engineering

About QpiAI

QpiAI is a deep tech AI and quantum computing startup building a scalable and AI-enabled quantum computer to enable the next-generation of extreme compute and automation. We are developing a vertically integrated AI and quantum solutions and building a full-stack scalable Quantum computer that brings value-based additions to the logistics, supply chain, finance, pharmaceuticals, automotive, and manufacturing industry.

Our verticals include AI Software, Quantum Software and Quantum hardware.

QpiAI Quantum hardware team is responsible for designing the Quantum Control Hardware and software interface to QpiAI Quantum Application.

Job Description

Role offers opportunity to work in hardware development of both QpiAI ASGP and Quantum Control circuits. Candidate will be part of the QpiAI Quantum Hardware team.

Requirements

Bachelor's/Master's degree in Electronics/Computer Engineering with 2-5 years' work experience preferred

- Hands on experience minimum 2years in RTL and application development on Xilinx

Ultrascale+ MPSoC and RFSoc Platforms.

- Architect and implement hardware/software-based DSP systems with the Xilinx RFSoc family of devices.
- Strong expertise in FPGA infrastructure IP, interrupts, AXI-Lite, AXI-Stream and AXI interconnect.
- Experience designing and modeling DSP blocks and algorithms for digital RF/SDR/5G system.
- Very good basics in Digital electronics and computer architecture.
- Solid understanding of DSP fundamentals.
- Understanding of RTL coding with Verilog, Module level functional simulation, verification and synthesis.
- Hands-on experience with FPGA prototyping with toolset and process flow of FPGAs from Xilinx Vivado, Vitis or Altera.
- Knowledge of Hardware Accelerators is a plus.
- Good understanding of ARM and RISC V CPU architecture and micro-architectures is a plus.
- Should be familiar with Linux system and scripting language with TCL/Python
- Proficiency in programming languages such as C and C++, Verilog, System Verilog.
- Good problem-solving skills and deductive abilities.
- Should be a good team player.

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